



VFD Interface Specification

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Description: This is the interface requirements of the multiplexed vfd

This list is in no way complete or accurate. It is offered only as a starting point for further discussion.

Electrical Connections:

- TP2 - 5 volts dc @ 1 amp continuous current
- TP4 – Pixel Data
- TP6 – Pixel Clock
- TP8 – Blank / Enable / Load Data
- TP10 - Ground

Bitstream:

- 35 Bits for top character
- 35 Bits for bottom character
- 2 zero bits
- 20 bits for column select

Operation:

- Pulse enable line to clear register data
- Clock 92 bit stream into registers.
- Pulse enable line to display column data
- Repeat for each column

Notes:

- Do not leave power applied without multiplex lines active